



HOLE TOLERANCE
UNLESS SPECIFIED
PLATED: +/- 3
NON-PLATED: +/- 1

FINISHED HOLES IN MILS			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY TOLERANCE/NOTES
1	12.0	PLATED	594 SEE NOTE 15
2	63.0	PLATED	11
3	115.0	NON-PLATED	3

SPECIFICATIONS:

- MATERIALS:
- MATERIAL FAMILY:
- CLADDING:
- SOLDER MASK:
- SILK SCREEN:
- SURFACE FINISH:
- INTENTIONAL SHORTS:
- TEST REQUIREMENTS:
- REQUIREMENTS:
1. REFER TO IPC-4101 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
2. ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION 1500119.
3. MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
4. HOLE PATTERN TOLERANCES FOR UNDESIGNED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
5. PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
6. HOLE DIAMETERS APPLY AFTER PLATING.
7. FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE DESIGNER'S INTENT ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
8. MINIMUM DESIGN LINE WIDTH IS .005 INCH.
9. MINIMUM DESIGN SPACING IS .005 INCH.
10. NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERMITTED AFTER CUSTOMER APPROVAL.
11. IF PAD REMOVAL IS REQUIRED, IT MUST BE ENOUGH TO MAINTAIN AN ANNUAL RING REQUIREMENT. WEAR MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNUAL RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)
12. THEFTING MAY BE ADDED TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN ONLY AFTER REVIEW AND APPROVAL FROM THE CUSTOMER.
13. IF THERE ARE CORNERS, FINISHES, NON-PLATED THROUGH HOLES, OR OTHER FEATURES TO BE 0.200 INCH MINIMUM, THERE SHALL BE NO THEFTING IN ANY AREAS FREE OF SOLDER MASK OR INTERNAL COPPER PLANES.
14. IFGER. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED.
15. A. DATE CODE (STAMP). B. DATE CODE (STAMP). C. LOT NUMBER.
16. REPAIRS PER IPC-711/21 (LATEST REV.) ARE ALLOWED.
17. DESIGNATED DRILL(S) TO BE FILLED WITH NON-CONDUCTIVE EPOXY AND MADE CO-PLANAR ON THE PRIMARY SIDE.
18. BOARDS MUST BE PALLETIZED AND SHIPPED INTACT. NO TABS ALLOWED ON AREAS WHERE THE SOLDER MASK IS OPEN.

4 LAYER STACKUP

- PRIMARY SILKSCREEN
- PRIMARY SOLDER MASK
- PRIMARY SIDE (LAYER 1) 50 OHMS
- GROUND PLANE (LAYER 2)
- SPLIT PWR PLANE (LAYER 3)
- SECONDARY SIDE (LAYER 4)
- SECONDARY SOLDER MASK
- SECONDARY SILKSCREEN
- CHARACTERISTIC IMPEDANCE - 50 OHMS +/- 10%
- ARTWORK LINE WIDTH FOR IMPEDANCE CONTROLLED LINES = 0.025"

PRIMARY SIDE

REVISIONS	DATE	APPROVAL
1	17APR14	J. BEDROSIAN
2		
3		
4		
5		
6		
7		
8		

ANALOG DEVICES	ASC DIVISION
17APR14	J. BEDROSIAN
17APR14	J. BEDROSIAN

TITLE	FABRICATION
ADL5246/7 EVAL BD Z	

SIZE	FEED NO	DIAMETER NUMBER	REV
D 2435509-038024			B

DO NOT SCALE DIM	2	1
SCALE	1/1	
SHEET	1 OF 1	